

PON-OR8-01-HPC

HPC e sistemi di archiviazione per la raccolta ed uso dati SRT.

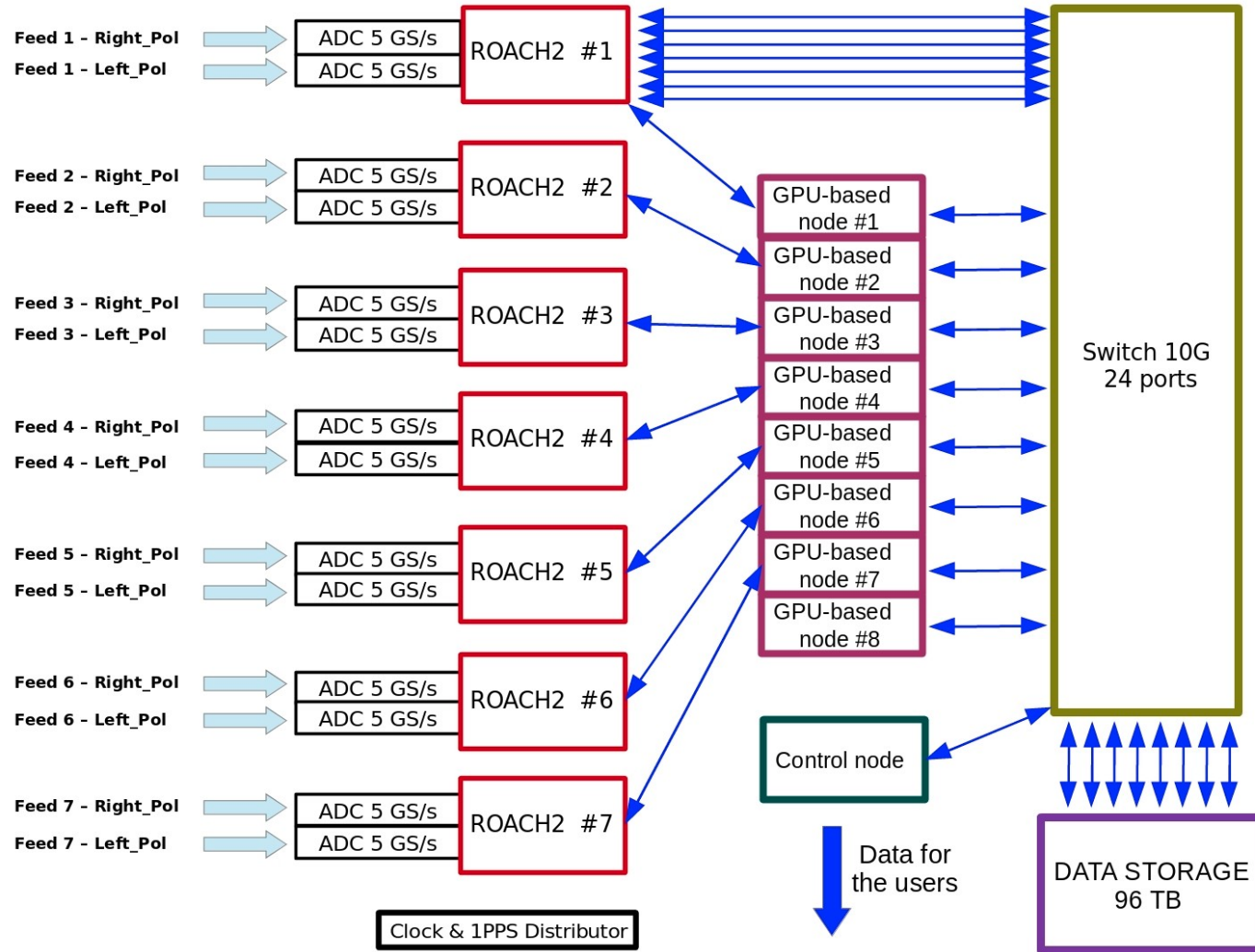
Evento di consultazione 8 maggio 2020

Backend digitali

**Andrea Melis, Raimondo
Concu**

Member of the PON-OR8 team

Sardinia Roach2-based Digital Architecture for Radio Astronomy (SARDARA)



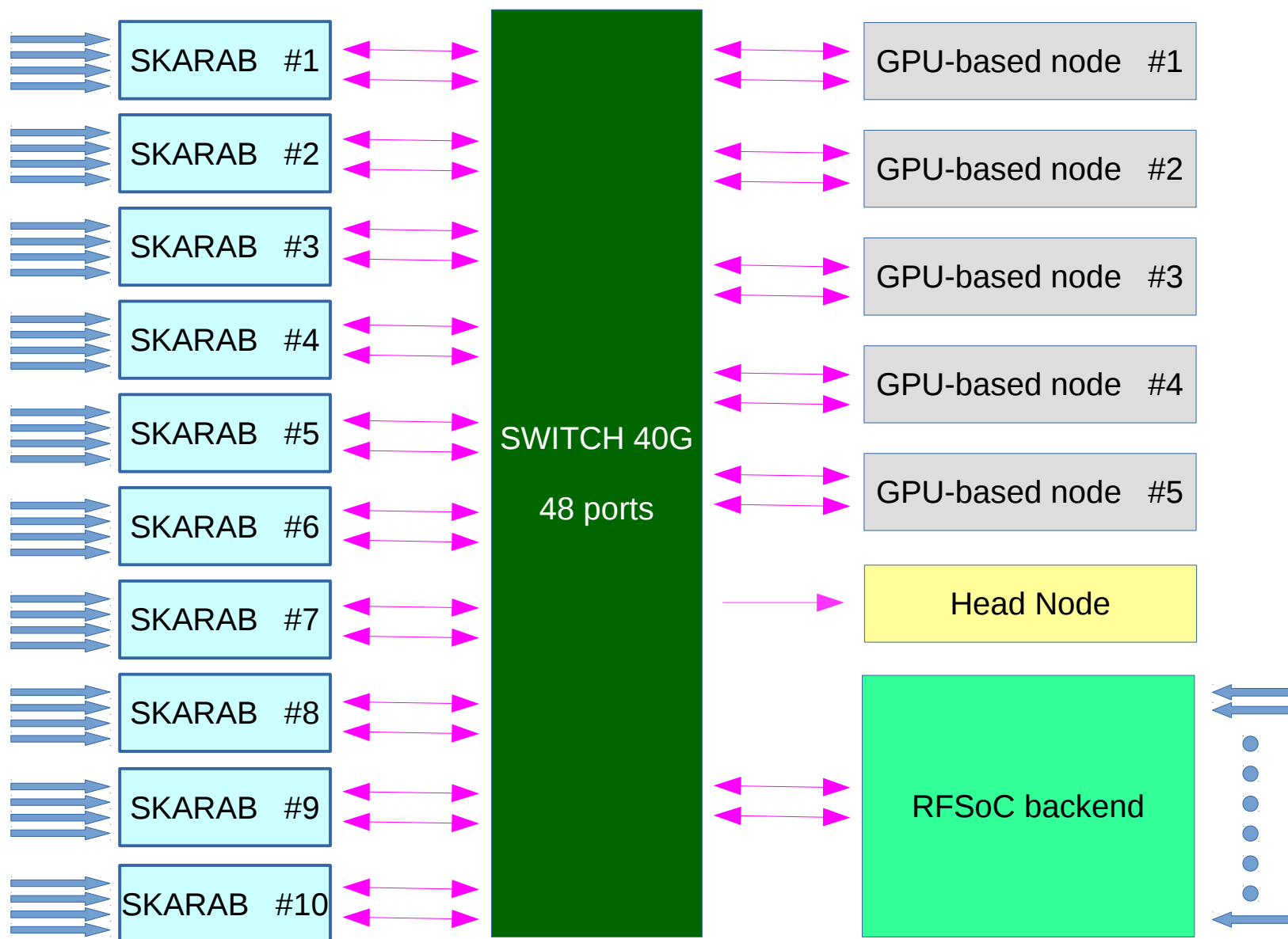
SARDARA computing node.

Chassis	Supermicro 4U E-ATX CSE-745TQ-R1200B
Motherboard	Supermicro X9DRI-F
CPU	2 Intel XEON 8 core E5-2640 v2
GPU	2 NVidia GTX 980 TI
NIC	Intel X520 2 × 10Gbps
Memory	256 GB DDR3
Hard Disk	4 Western Digital 4 TB

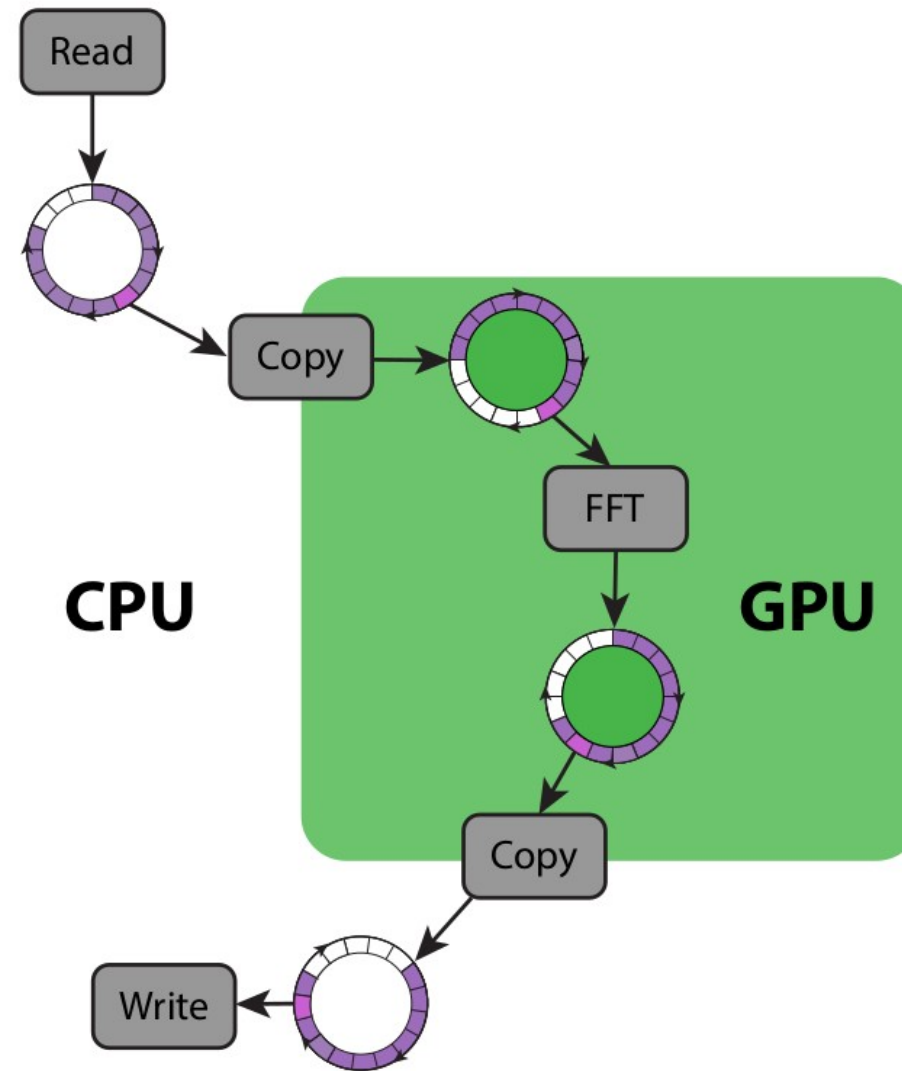
Switch 10G 24 ports: Brocade VDX 6720

Data storage: Fujitsu ETHERNUS DX80/90 S2

SKARAB-based Digital Architecture for Radio Astronomy (SKADARA)



Pipeline radio astronomica sui dati grezzi



Processing CPU-GPU

